Exhibit 41

IN THE UNITED STATES DISTRICT COURT FOR THE WESTERN DISTRICT OF TEXAS WACO DIVISION

ACQIS LLC, Plaintiff	§ § 6-20-CV-00966 §
-V-	Š
ACHICEPLY COMPLETED INC	§
ASUSTEK COMPUTER, INC.,	§
Defendant	§
	<u> </u>
ACQIS LLC,	Š
Plaintiff	§ 6-20-CV-00968
33	Š
-V-	Š
	Š
WIWYNN CORPORATION,	Š
Defendant	Š
J	Š

ORDER MEMORIALIZING WHETHER PRIOR CONSTRUCTIONS APPLY TO TERMS IN THIS CASE

The Court held a hearing on December 19, 2022 to determine whether constructions for terms that Judge Burroughs's construed in a prior case (*EMC*) and which the Federal Circuit recently affirmed apply to terms in this case. During the hearing, the Court provided its decisions. This order memorializes those decisions.

SIGNED this 28th day of December, 2022.

ALAN D ALBRIGHT

UNITED STATES DISTRICT JUDGE

Term Construed by Claim Term Groups in Question		Court's Final Ruling
Judge Burroughs in the <i>EMC</i> case		
"Communicating a PCI bus transaction"	 "Full Transaction" terms (e.g., '416 Patent, Claim 60: "for communicating encoded serialized Peripheral Component Interconnect (PCI) bus transaction datawherein the serial bit stream of PCI bus transaction comprises encoded PCI address and data bits." "Address and Data" terms (e.g., RE'814) 	 "Full Transaction": The EMC construction applies to these terms. "Address and Data": The EMC construction does
	Patent, Claim 31: "to communicate address and data bits of PCI bus transactions in serial form")	not apply to these terms.
"encoded serial bit stream of a PCI bus transaction"	• "Encoding" terms – claims reciting an "encoded" PCI bus transaction	EMC construction only applies to terms that recite (1) "encoded," (2) "serial," and (3) "PCI bus transaction."
	• "Serial" terms – claims reciting a PCI bus transaction in "serial stream" or "serial form", but do not recite "encoding" the PCI bus transaction or an "encoded" PCI bus transaction:	Note: The Court does not find it necessary to construe (1) "encoded," (2) "serial," and (3) "PCI bus transaction" as Judge Burroughs already construed—and the Circuit affirmed—"PCI bus transaction" individually and "encoded" and "serial" as part of "encoded serial bit stream of a PCI bus transaction."